

Notice of References Cited	Application/Control No. 09/852,164	Applicant(s)/Patent Under Reexamination FAINER ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,882,739	11-1989	Potash et al.	375/358
	B	US-5,226,120	07-1993	Brown et al.	709/224
	C	US-5,422,814	06-1995	Sprague et al.	701/213
	D	US-5,440,313	08-1995	Osterdock et al.	342/352
	E	US-5,757,786	05-1998	Joo, Hwan-Yong	370/324
	F	US-5,854,793	12-1998	Dinkins, Gilbert M.	370/503
	G	US-5,953,384	09-1999	Walsh et al.	375/354
	H	US-6,016,322	01-2000	Goldman, Herbert B.	370/508
	I	US-6,057,863	05-2000	Olarig, Sompong P.	345/520
	J	US-6,130,894	10-2000	Ojard et al.	370/421
	K	US-6,134,234	10-2000	Kapanen, Jouko Juhani	370/350
	L	US-6,324,586 B1	11-2001	Johnson, Aric R.	709/248
	M	US-6,405,104 B1	06-2002	Dougherty, John James	700/292

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Howe, Denis, "Field Programmable Gate Array" The Free On-Line Dictionary of Computing © 1993-2004, (www.dictionary.com) July 11, 1997.			
	V	Howe, Denis, "Peripheral Component Interconnect" The Free On-Line Dictionary of Computing © 1993-2004, (www.dictionary.com) December 7, 1997.			
	W	Phillips, B.; "Have storage area networks come of age?" Computer, Volume 31, Issue: 7, July 1998, Pages 10-12.			
	X	Molero, X. et al.; "On the effect of link failures in fibre channel storage area networks" Paralle Architectures, Algorithms and Networks, I-SPAN, Proceedings, International Symposium on, Dec. 7-9, 2000, Pages 102-111.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,421,723 B1	07-2002	Tawil, Ahmad Hassan	709/224
	B	US-6,434,626 B1	08-2002	Prakash et al.	709/238
	C	US-6,477,143 B1	11-2002	Ginossar, Dror	370/230
	D	US-6,636,499 B1	10-2003	Dowling, Mary G.	370/338
	E	US-6,643,586 B1	11-2003	Allen et al.	701/213
	F	US-6,651,099 B1	11-2003	Dietz et al.	709/224
	G	US-6,665,267 B1	12-2003	Iwasaki et al.	370/241
	H	US-6,754,210 B1	06-2004	Ofek, Yoram	370/389
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Molero, X. et al.; "Performance analysis of storage area networks using high-speed LAN interconnects" Networks, Proceedings, IEEE Internation Conference on, Sept. 5-8, 2000, Pages 474-478.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.